

TRANSMITTAL LETTER (General - Patent Pending)

Docket No. END920000094US1

on Of: David J. Alcoe

Serial No.

Filing Date

Examiner

Group Art Unit

09/714,373

11/16/2000

Lindinger, M.

2841

Title: COMPLIANT LAMINATE CONNECTOR

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith is:

Amendment

in the above identified application.

- No additional fee is required.
- A check in the amount of

is attached.

- The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. 09-0457(IBM) as described below. A duplicate copy of this sheet is enclosed.
 - Charge the amount of
 - \boxtimes Credit any overpayment.
 - X Charge any additional fee required.

Dated: 2/18/2003

Jack P. Friedman Reg. No. 44,688 Schmeiser, Olsen & Watts 3 Lear Jet Lane, Suite 201

Latham, NY 12110 (518) 220-1850

certify that this document and fee is being deposited with the U.S. Postal Service as on2/18/2003

first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C.

20231.

Signature of Person Mailing Correspondence

Kim Dwileski

Typed or Printed Name of Person Mailing Correspondence



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Applicants: David J. Alcoe)	Examiner: Lindinger, M.	WIN MAY D
Serial No.: 09/714,373 Filing Date: 11/16/00)))	Art Unit: 2841	1. Tylor CE. 3/4/03
Title: COMPLIANT LAMINATE CONNECTOR)		RECEIVE
Commissioner for Patents Box AF Washington D.C. 20231			ROOM

AMENDMENT under 37 C.F.R. §1.116(a)

Sir:

This paper is being filed in response to the Final Office Action dated December 26, 2002.

Reconsideration and allowance are respectfully requested in view of the Remarks below, that each of the presently pending claims be allowed, and that the application be passed to issue.

IN THE CLAIMS:

Claim 43 is amended herein, as marked up in Appendix A. Claims 1, 6-7, 20, 22-27, 31, and 37were previously amended prior to the present office action response. Currently pending claims 1, 3-20, and 22-44 for consideration by the Examiner are as follows:

- 1. (Previously Amended) An electronic device, comprising:
 - a first substrate having a first coefficient of thermal expansion;
 - a second substrate having a second coefficient of thermal expansion; and